

Honeywell's Docket No. H0001609 (4960)
Pratt & Whitney's Docket No. 665.22-US1

RECEIPT #1
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Stephen S. OHR

Application No.: 09/752,538

Group No.: 2841

Filed: 12/28/2000

Examiner: Not Yet Assigned

For: LAYERED CIRCUIT BOARDS AND METHODS OF PRODUCTION THEREOF

Assistant Commissioner for Patents
Washington, D.C. 20231

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REQUEST FOR CORRECTED FILING RECEIPT

1. Attached is a copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.
2. There is an error with respect to the following, which is incorrectly entered.

CERTIFICATE OF MAILING/TRANSMISSION (37 C.F.R. section 1.8a)

I hereby certify that this correspondence is, on the date shown below, being:

MAILING



☒ deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

FACSIMILE

☐ transmitted by facsimile to the Patent and Trademark Office.

Kristin J. Azcona

Date: March 1, 2001

Error in

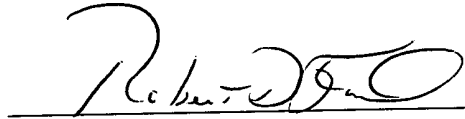
Correct data

1. Attorney Docket Number

1. H0001609 (4960)

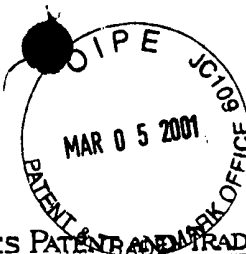
3. The correction is not due to any error by applicant and no fee is due.

Date: March 1, 2001



Robert D. Fish
Fish & Associates, LLP
1440 N. Harbor Blvd.
Suite 706
Fullerton, CA 92835
USA

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/752,538	12/28/2000	2841	710	885-22-US1 <i>H0001609(4960)</i>	4	18	3

CONFIRMATION NO. 1429

FILING RECEIPT



Honeywell International Inc.
Law Dept. AB2
P.O. Box 2245
101 Columbia Road
Morristown, NJ 07962

Date Mailed: 02/14/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Stephen S. Ohr, Chippewa Falls, WI;

Continuing Data as Claimed by Applicant

Foreign Applications

If Required, Foreign Filing License Granted 02/14/2001

Projected Publication Date: 07/04/2002

Non-Publication Request: No

Early Publication Request: No

Title

Layered circuit boards and methods of production thereof

Preliminary Class

174

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